Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	154	361/737.ccls. and ((smart adj card) or (memory adj card) or (IC adj card)) and (antenna or coil)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/09 08:29
L2	0	361/737.ccls. and ((smart adj card) or (memory adj card) or (IC adj card)) and (antenna or coil) and misreplacement	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/09 08:29
L3	58	361/737.ccls. and ((smart adj card) or (memory adj card) or (IC adj card)) and (antenna or coil) and substrate	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/09 08:31
L4	2	361/737.ccls. and ((smart adj card) or (memory adj card) or (IC adj card)) and (antenna or coil) and substrate and "RFID"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/09 08:32
L5	633	(motherboard or (circuit adj board)) with (open adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/09 08:36
L6	1	(motherboard or (circuit adj board)) with (open adj circuit) with (zero adj ohm adj resistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/09 08:33
L7	2	5 and (zero adj ohm adj resistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/09 08:33
L8	2	(motherboard or (circuit adj board)) with (open adj circuit) with receivers with controller	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/09 08:34
L9	152267	5 receivers and controller	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/09 08:34

L10	31	5 and receivers and controller	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/09 08:34
L11	24	5 and receivers and controller and resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/09 08:35
L12	4	5 and receivers and controller and jumper	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/09 08:35
L13	16	(motherboard or (circuit adj board)) with (open adj circuit) with resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/09 08:37
L14	7	(motherboard or (circuit adj board)) near6 (open adj circuit) with resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/09 08:39
L15	16	(circuit adj board) with (open adj circuit) with resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/09 08:39
L16	1414	network with (open adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/09 08:40
L17	621	network near5 (open adj circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/09 08:40
L18	0	network near5 (open adj circuit) with (zero adj ohm adj resistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/09 08:40
L19	1	network with (open adj circuit) with (zero adj ohm adj resistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2006/01/09 08:41

L20	2	("5905885").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	OFF	2006/01/09 08:41